THE UNITED STATES PATENT AND TRADEMARK OFFICE

281/ Ufw

Applicant:

Lie Y. Ahn et al.

Title:

STRUCTURES,

METHODS, AND SYSTEMS FOR FERROELECTRIC MEMORY

TRANSISTORS

Docket No.:

Examiner:

303.716US2

Filed:

June 20, 2003

Tuan H. Nguyen

Serial No.: 10/600,965

Due Date: June 13, 2004 (Sunday)

Group Art Unit: 2811

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

X A return postcard.

X A Response to Restriction Requirement (1 Page).

X A Communication Concerning Related Applications (1 pg.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

Atty. Eduardo E. Drake-

Reg. No. 40.594

<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this <u>14th</u> day of June, 2004.

Name

Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

PATENT S/N 10/600,965

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

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COMMUNICATION CONCERNING RELATED APPLICATION(S)

Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No. 09/383726	Filing Date August 26,	Attorney Docket 303.621US1	<u>Title</u> WEAK FERROELECTRIC TRANSISTOR
6498362 09/809560 6586792	1999 March 15, 2001	303.716US1	STRUCTURES, METHODS, AND SYSTEMS FOR FERROELECTRIC
0300772	2001		MEMORY TRANSISTORS
10/320652	December 16, 2002	303.621US2	WEAK FERROELECTRIC TRANSISTOR
10/320253	December 16, 2002	303.621US3	METHOD OF FORMING A WEAK FERROELECTRIC TRANSISTOR

Respectfully submitted,

KIE Y. AHN ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN_55402

(612) 349-9593/

Date 14 Jone WA

Eduardo E. Drake

Reg. No. 40,594

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Signature

<u>S/N 10/600,965</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kie Y. Ahn et al.

Examiner: Tuan Nguyen

Serial No.:

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STRUCTURES, METHODS, AND SYSTEMS FOR FERROELECTRIC

MEMORY TRANSISTORS

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed May 13, 2004, Applicant elects, without traverse, Group II which includes claims 17-31. Applicant respectfully cancels remaining claims 32-42 (Group I) without prejudice, and reserves the right to reintroduce them in a divisional application at a later date.

The Examiner is invited to contact Applicant's Representatives at the below-listed telephone number if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

Respectfully submitted,

KIE Y. AHN ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9593

Date /4 Jone 204

Eduardo E Drake

Reg. No. 40,594

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